



# NEWSLETTER

**OCTOBER 2025-JANUARY 2026**

**VOL.7, ISSUE:2**

## **DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

### **INSTITUTE VISION**

Producing globally competent and quality technocrats with human values for the holistic needs of industry and society.

### **INSTITUTE MISSION**

- Creating an outstanding infrastructure and platform for enhancement of skills, knowledge and behaviour of students towards employment and higher studies.
- Providing a healthy environment for research, development and entrepreneurship, to meet the expectations of industry and society.
- Transforming the graduates to contribute to the socio-economic development and welfare of the society through value based education.

### **DEPARTMENT VISION**

To be a center of excellence in imparting knowledge, skills and ethical values, while fostering innovation, sustainability and globally competent to make exemplary contributions to the field of Electrical and Electronics Engineering.

### **DEPARTMENT MISSION**

- To impart technical education using state-of-the-art infrastructure, laboratories, and instructional methods, ensuring students acquire comprehensive knowledge and skills.
- To foster industry-oriented learning by facilitating internships, industrial visits, collaborative projects with industries.
- To create a congenial environment for higher education, employment, and entrepreneurship by delivering quality education, enhancing professional skills and promoting research and innovation.
- To promote societal commitment and ethical leadership by instilling moral values and encouraging responsible engineering practices among students.

### **PROGRAM EDUCATIONAL OBJECTIVES**

- Graduates will possess a strong foundation in core and interdisciplinary areas of Electrical and Electronics Engineering along with analytical and computational skills, enabling them to tackle global challenges through innovative and critical problem-solving
- Graduates will actively engage in research, entrepreneurship, and innovation to address contemporary challenges in Electrical and Electronics Engineering while promoting sustainable and inclusive technological development for the betterment of society.
- Graduates will exhibit effective communication skills, collaborative abilities, and ethical values, preparing them for successful careers, higher education, and leadership roles in a rapidly evolving competitive environment.

### **PROGRAM SPECIFIC OUTCOMES**

- PSO1: Capable of design, develop, test, verify and implement electrical and electronics engineering systems and products.
- PSO2: Succeed in national and international competitive examinations for successful higher studies and employment.

# RESEARCH CONTRIBUTIONS

1. Dr. B. Parusharamulu published a paper with title “ Feature-driven fruit classification using leveraging machine learning algorithms and nano semiconductor sensors” in International Journal of Nano Dimension, October 2025.
2. Mr. K. Veda Prakash, Mrs. T. Sravya published a paper with title “ AI-Driven Adaptive Power Management system with Multi-source DC supply, PI and PID Motor Control” in International Journal of Environmental Sciences, September 2025.
3. Dr. B. V. S. Acharyulu published a paper with title “Capacitor and Super capacitor reliability analysis utilizing NEO-FUZZY Neural Learning” in Journal of Theoretical and Applied Information Technology, October 2025.
4. Dr. K. Subbaramaiah published a paper with title “Hybrid DSTATCOM Design Using Deep Belief Networks for Enhanced Power Quality Assessment” in Power Electronics and Drives, January 2026.
5. Dr. M. Venkateswaran published a paper with title “ Integrating Fault Detection and Output Regulation in Electric Vehicle Power Converters” in Journal of Circuits, Systems and Computers, January 2026.
6. Dr. M. Venkateswaran, Mr. K. Ravi Sankar published a paper with title “ A compact dual-output Boost-SEPIC converter with digital predictive current control for automotive accessory power applications” in Engineering Research Express, January 2026.
7. A. Anil Kumar published a paper with title “ Hybrid Passive-Active Anti-Islanding Methods for PV Systems: NDZ Reduction and Multi-Inverter Interaction Analysis” in 2025 International Conference on Communication, Computer, and Information Technology (IC3IT), January 2026.
8. Dr. M. Venkateswaran, Dr. K. Praveen Kumar Yadav published a paper with title “Artificial Intelligence–Enabled Energy Management: A Mixed-Methods Evaluation of Performance” in 2025 International Conference on Modern Sustainable Systems (CMSS), October 2025.
9. Dr. B. Parusharamulu, Mrs. K. Anitha, Mrs. K. Nagamani published a paper with title “ FPGA Based SPWM Control for Three Phase Three Level Inverter” in 2025 International Conference on Power Electronics and Energy (ICPEE), December 2025
10. Mr. J. Vijaychandra published a book chapter with title “ Scaled Conjugate Gradient Control Machine Learning Algorithm for Switching of Boost Converters Employed in a Grid Tied Solar PV Systems” in AI-Driven Solutions and Emerging Power Technologies for Sustainable Future: Proceedings of International Conference, ETAEE 2024 (Springer), January 2026.

# RESEARCH CONTRIBUTIONS

- 1.Mr.J.Vijaychandra published a book chapter with title “ Analysis of Solar PV-Powered Reduced Component Switched-Capacitor-Based Multilevel Inverter” in Multilevel Inverter Technology for Renewable Energy Systems: Enhancing Efficiency and Sustainability Through Advanced Power Conversion Solutions (Springer), January 2026.
- 2.Mr.J.Vijaychandra, Dr.B.Parusharamulu published a book chapter with title “ Novel Switched Capacitor Self-Balanced Reduced Switch Multilevel Inverter for Photovoltaic System” in Multilevel Inverter Technology for Renewable Energy Systems: Enhancing Efficiency and Sustainability Through Advanced Power Conversion Solutions (Springer), January 2026.
- 3.Mr.J.Vijaychandra published a book chapter with title” Teaching learning-based optimized artificial neural network for predicting the maximum power point of a large-scale grid-connected solar PV system” in Advanced Analytics for Reliability and Resilience of Energy System (Elsevier), January 2026.
- 4.Mr.J.Vijaychandra published a book with title “ Fundamentals of Electric Vehicles” in Iterative International Publishers, December 2025

## FACULTY INSIGHTS



**DR.K.SUBBARAMAIAH, PROFESSOR AND HOD, EEE, LIET ACTED AS SESSION CHAIR AT 3RD INTERNATIONAL CONFERENCE ON ARTIFICIAL INTELLIGENCE, COMPUTING TECHNOLOGIES, INTERNET OF THINGS (IOT), AND DATA ANALYTICS (AICTA 2025)**



**MR.B.MURALI, ASSISTANT PROFESSOR, EEE, LIET HAS BEEN ACTED AS JUDGE FOR BRAINO VISION HACKATHON EVENT, VIJAYAWADA**

# FACULTY INSIGHTS



**THE DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING HAS SUCCESSFULLY REGISTERED THREE INNOVATIVE PRODUCTS UNDER THE MICRO, SMALL AND MEDIUM ENTERPRISES (MSME) INITIATIVE, DEMONSTRATING ITS CONTRIBUTION TO PRODUCT DEVELOPMENT, ENTREPRENEURSHIP, AND INDUSTRY-ORIENTED RESEARCH. DR B.V.S.ACHARYULU, PROFESSOR DEPARTMENT OF EEE RECEIVED THE BEST PERFORMING INSTITUTION AWARD FOR ANSYS ELECTROMAGNETIC ANALYSIS @EDUSKILLS CONNECT 2025**



**MR. VIJAYCHANDRA JODDUMAHANTHI HAS BEEN SELECTED AMONG THE TOP 20 FINALISTS OF THE PRESTIGIOUS CLEAR SKIES CHALLENGE 2025 – OPERATION DRONAGIRI, A NATIONAL-LEVEL INNOVATIONCHALLENGE. DEMONSTRATING EXCEPTIONAL TECHNICAL EXPERTISE AND INNOVATIVE THINKING, HE FURTHER SECURED THIRD POSITION AMONG THE FINALISTS AND WAS AWARDED A CASH PRIZE OF ₹1,00,000. THIS REMARKABLE ACCOMPLISHMENT REFLECTS HIS STRONG COMMITMENT TO RESEARCH, INNOVATION, AND EXCELLENCE IN EMERGING TECHNOLOGIES, AND BRINGS GREAT HONOR TO THE INSTITUTION.**



# STUDENTS PARTICIPATION



DEVI AND HER TEAM FROM FINAL YEAR BTECH,EEE SECURED SECOND POSITION IN STEP CONE 2026, A NATIONAL LEVEL TECHNICAL SYMPOSIUM AT GMRIT, ANDHRA PRADESH AND WON RS.7000/- AS THE CASH PRIZE FOR SHOWCASING THEIR PROJECT ON PEER TO PEER ENERGY TRADING.



**M.Dhanalakshmi**  
**22KD1A026**

M.DHANALAKSHMI FROM FINAL BTECH EEE HAS PRESENTED HER PAPER WITH TITLE “ENVISION OF SATELLITE LOS/NLOS USING TLBO POWERED SCALED CONJUGATE GRADIENT NEURAL NETWORK” IN 3RD INTERNATIONAL CONFERENCE ON ARTIFICIAL INTELLIGENCE, COMPUTING TECHNOLOGIES, INTERNET OF THINGS (IOT), AND DATA ANALYTICS (AICTA 2025). IN THIS STUDY, SCALED CONJUGATE GRADIENT (SCG) METHOD IS USED TO TRAIN ARTIFICIAL NEURAL NETWORKS (ANNS) TO PREDICT THE SATELLITE LOS/NLOS. IN ADDITION TO THIS, THE SYNAPTIC WEIGHTS OF THE PROPOSED NEURAL NETWORK ARE OPTIMIZED USING TEACHING-LEARNING BASED OPTIMIZATION ALGORITHM. NLOS (NON-LINE OF SIGHT) CORRESPONDS TO A PATH WHERE THE DIRECT SIGNAL IS OBSTRUCTED AND THE SIGNAL REACHES THE RECEIVER BY MEANS OF REFLECTIONS OR OTHER HIDDEN PATHS. IN THE SCENARIO OF SATELLITE COMMUNICATION, LOS (LINE OF SIGHT) USUALLY SIGNIFIES THE DIRECT PATH WITHIN THE SATELLITE AND THE RECEIVER.

A.VAMSI AND P.THARUN FORM FINAL BTECH EEE, LIET WON THE CONSOLATION PRIZE WITH A CASH AWARD OF RS.5000/- IN A HACKATHON EVENT CONDUCTED BY ANITS COLLEGE OF ENGINEERING, VISAKHAPATNAM

## LENDI E-IOT

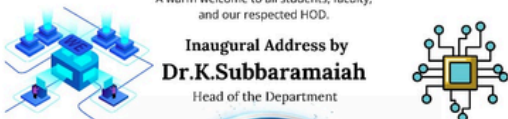


**WELCOME TO THE INAUGURATION  
OF**

**LENDI E-IOT CLUB**

A warm welcome to all students, faculty,  
and our respected HOD.

Inaugural Address by  
**Dr.K.Subbaramaiah**  
Head of the Department



### Event Emphasis

- ROADMAP FOR IOT & EMBEDDED ACTIVITIES
- STUDENT OPPORTUNITIES & UPCOMING PROJECTS
- INTERACTIVE SESSION INTRODUCTION TO E-IOT CLUB VISION & MISSION

23.01.2026

EEE e-class room

THE INAUGURATION CEREMONY OF THE LENDI E-IOT CLUB MARKS A SIGNIFICANT STEP TOWARD FOSTERING INNOVATION, TECHNICAL EXCELLENCE, AND PRACTICAL LEARNING IN THE FIELDS OF EMBEDDED SYSTEMS AND INTERNET OF THINGS (IOT). THE CEREMONY FEATURES AN INAUGURAL ADDRESS BY DR. K. SUBBARAMAIAH, HEAD OF THE DEPARTMENT, WHO EMPHASIZES THE IMPORTANCE OF INNOVATION, INTERDISCIPLINARY LEARNING, AND STUDENT-DRIVEN TECHNICAL ACTIVITIES. THE EVENT ALSO HIGHLIGHTS THE ROADMAP OF THE E-IOT CLUB, UPCOMING PROJECTS, WORKSHOPS, AND OPPORTUNITIES FOR STUDENTS TO ENGAGE IN HANDS-ON LEARNING, COLLABORATIVE DEVELOPMENT, AND REAL-TIME EMBEDDED APPLICATIONS.

# STUDENT PLACEMENTS



**lendi**

Department of  
Electrical & Electronic Engineering

**2026**  
Placements

**3.6**  
LPA

*Congratulations*

for being placed in

**Infosys**



**E.Brunda**  
22KD1A0231



**K.Leelavathi**  
22KD1A0245



**K.Sravani**  
22KD1A0250



**M.Dhanalaxmi**  
22KD1A026



**N.Hymavathi**  
22KD1A0278



**P.Sri Mahalaxmi**  
22KD1A0284



**S,Dhanalaxmi**  
22KD1A02A3



**U.Teja**  
22KD1A02B8



**V.NavyaSri**  
22KD1A02C0



**K.Sai Sailaja**  
23KD5A0209



**M.Geetanjali**  
23KD5A0210

THE DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING AT LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY PROUDLY CONGRATULATES ITS STUDENTS ON THEIR SUCCESSFUL PLACEMENT IN INFOSYS FOR THE ACADEMIC YEAR 2026. THIS ACHIEVEMENT REFLECTS THE STUDENTS' DEDICATION, TECHNICAL COMPETENCE, AND CONTINUOUS EFFORTS TOWARD ACADEMIC AND PROFESSIONAL EXCELLENCE. SECURING PLACEMENTS WITH A PACKAGE OF 3.6 LPA, THE SELECTED STUDENTS HAVE DEMONSTRATED STRONG SKILLS, DETERMINATION, AND READINESS TO MEET INDUSTRY STANDARDS. THE DEPARTMENT TAKES IMMENSE PRIDE IN THEIR ACCOMPLISHMENT AND APPRECIATES THE GUIDANCE OF FACULTY MEMBERS AND THE SUPPORT PROVIDED THROUGH TRAINING, SKILL DEVELOPMENT, AND PLACEMENT ACTIVITIES. THIS MILESTONE SERVES AS AN INSPIRATION FOR FELLOW STUDENTS TO STRIVE FOR SUCCESS AND BUILD PROMISING CAREERS IN THE FIELD OF ENGINEERING AND TECHNOLOGY.